

**PATENT PWV1.PAU.49** 

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Gurvich et al.

Serial No.:

10/633,984

Art Unit:

2817

Filed:

August 4, 2003

Examiner:

Henry Choe

Title:

**Enhanced Efficiency LDMOS** 

**Based Feed Forward** 

Amplifier

## INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. § 1.56, Applicant(s) submit herewith the attached Information Disclosure Statement Form PTO-1449 in connection with the aboveidentified patent application. The statement is not a representation that all of the information cited is necessarily effective as prior art against the application.

Each item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart PCT application not more than three months prior to the filing of the Information Disclosure Statement. Therefore, it is respectfully submitted no fee is required. If any fee is required for consideration of this Information Disclosure Statement please charge Deposit Account No. 01-1960.

Respectfully submitted,

Date: May 26, 2004

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LIST OF PATENTS AND PUBLICATIONS
FOR APPLICANTS INFORMATION
DISCLOSURE STATEMENT

(USE SEVERAL SHEETS IF NECESSARY)

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GROUP: 2817

Reference Designation

U.S. PATENTS

Examiner Initial	Document Number	Date	Name	Class	Filing Date Subclass if appropriate
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